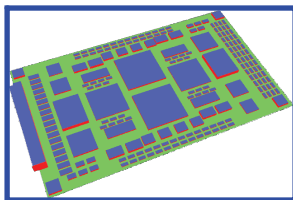
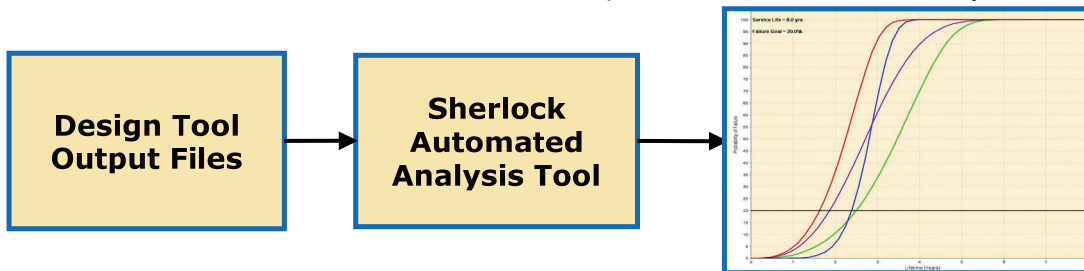




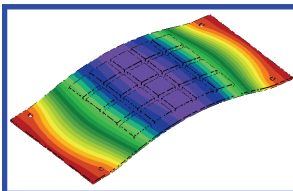
Automated Design Analysis (ADA)

In-depth Physics of Failure (PoF) based reliability analysis has always been the victim of cost tradeoff. Simple numerical and statistical analysis have sufficed, as anything more required expensive tools and highly skilled engineers to operate them. As a result, industry has been married to outdated statistical models and are reluctant to invest in extensive reliability analysis before product launch. The return on investment has often been overshadowed by the initial cost. Until now.

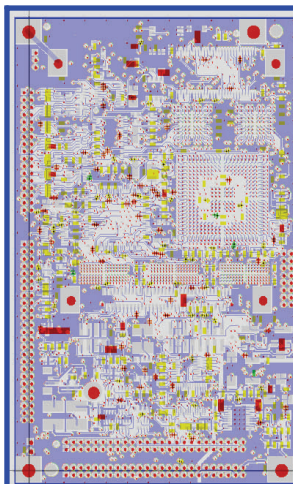
Using only the files generated by traditional design tools, DfR's ADA software can perform a full range of PoF reliability analyses without requiring days or weeks of labor. Contact your DfR Solutions Representative or call the number below to learn how to put this holistic tool to work for you.



Thermal: Calculate the risk from thermal stresses from temperatures dissipated from the components.



Plated Through Hole (PTH) Fatigue: Computes the mean fatigue life for printed circuit board materials based on the type and thickness of the material, PTH size, and thermal environment.



Vibration – Harmonic and Shock: Calculate the fundamental harmonic frequencies of your design without the hassle of full FEA modeling. Using the inputs already being generated by your EDA tool and its automated meshing algorithms, DfR's ADA Software can provide you with a vibration analysis. Also understand your design's susceptibility to shock damage before a single prototype is built.

CAF: Calculate the risk of cathodic anodic filament formation based on trace separation, material selection and the intended use environment.

Solder Joint Fatigue: Predicts the lifetime of leadless chip components (LCCs) as a function of solder material, component type, PCB properties, and use environment.

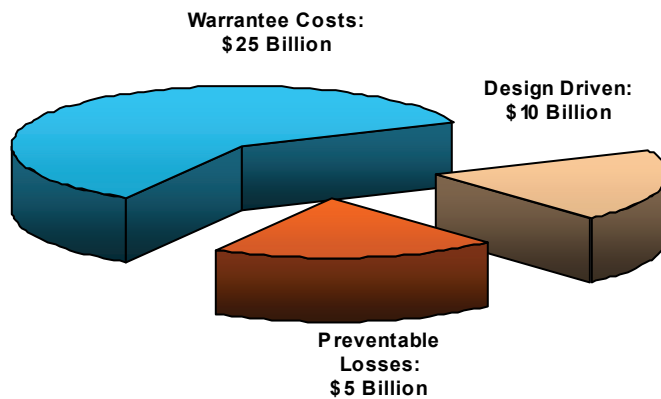
Automated Design Review: From component selection, to circuit analysis. From material compatibility to manufacturability. The power of a DfR Solutions Initial Reliability Assessment in an automated format.



How do you save \$5,000,000,000.00?

Five billion dollars. That's how much the electronics industry pays out in preventable design-driven warranty claims.

The worldwide market for electronics equipment approaches \$1 trillion per year. A review of warranty data from Warranty Week and financials in SEC filings indicates that warranty returns vary between 1 to 4% of revenue with an average of approximately 2.5% - or \$25 Billion industry-wide. 30 to 50% of warranty returns are design driven – creating **\$8 to \$12 Billion in design-driven warranty claims per year!** DfR believes that the use of automated reliability assessment tools, such as DfR's ADA Software, will reduce design-driven warranty returns by 25 to 50%.



Even though design typically represents only 5% of a product's cost, it influences approximately 80% of total product development spending. Getting the design right - the first time - can make the difference between a profitable and popular product and an unprofitable one. DfR's ADA Software addresses three major trends in product design:

- Increased pressure for faster product development cycles
- Effects of product quality and reliability on profitability and reputation
- Design activities being assigned to "off-shore" subject matter experts located at or near manufacturing facilities

The future is bright. Make sure your product survives to see it. **Call today to schedule your free demonstration.**

System Requirements	
Operating System	Windows 2003; Windows XP 32 Windows XP 64; Windows Vista
Processor	Required: Single Core: 3GHz 32-bit (x86), 3GHz 64-bit (x64); Dual Core: 2GHz 32-bit (x86), 2GHz 64-bit (x64) Recommended: Dual Core: 2.8GHz 64-bit (x64); Quad Core: 2.4GHz 64-bit (x64)
Disk Space	20GB Hard Drive Recommended; 20MB Installation 100MB Recommended for project files
RAM	2048 MB (Required) 4096 MB (Recommended)
Java (JVM)	Java Runtime Environment Versions 1.0.6.0_10 and higher